Switching Regulator IC for Buck Converter

Current Mode Control w/ 40V/1A MOSFET

**GENERAL DESCRIPTION**

The **NJW4153** is a buck converter with **40V/1A** MOSFET. It corresponds to high oscillating frequency, and Low ESR Output Capacitor (MLCC) within wide input range from 4.6V to 40V. Therefore, the **NJW4153** can realize downsizing of applications with a few external parts so that adopts current mode control.

Also, it has a soft start function, an over current protection and a thermal shutdown circuit.

It is suitable for power supply circuit of Micro Processor, DSP and so on that need fast transient response.

**FEATURES**

- Current mode Control
- Maximum Rating Input Voltage 45V
- Wide Operating Voltage Range 4.6V to 40V
- Switching Current 1.4A min.
- PWM Control
- Built-in Compensation Circuit
- Correspond to Ceramic Capacitor (MLCC)
- Oscillating Frequency 1MHz typ. (A ver.)
- Soft Start Function 4ms typ.
- UVLO (Under Voltage Lockout)
- Over Current Protection (Hiccup type)
- Thermal Shutdown Protection
- Standby Function
- Package Outline  
  - NJW4153U2 : SOT-89-5  
  - NJW4153KV1 : ESON8-V1

**PRODUCT CLASSIFICATION**

<table>
<thead>
<tr>
<th>Part Number</th>
<th>Version</th>
<th>Oscillation Frequency</th>
<th>Package</th>
<th>Operating Temperature Range</th>
</tr>
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<tbody>
<tr>
<td>NJW4153U2-A</td>
<td>A</td>
<td>1MHz typ.</td>
<td>SOT-89-5</td>
<td>General Spec. -40°C to +85°C</td>
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<tr>
<td>NJW4153KV1-A</td>
<td>A</td>
<td>1MHz typ.</td>
<td>ESON8-V1</td>
<td>General Spec. -40°C to +85°C</td>
</tr>
</tbody>
</table>
PIN CONFIGURATION

- PIN FUNCTION
  1. ON/OFF
  2. GND
  3. IN-
  4. SW
  5. V+

PIN FUNCTION
1. SW
2. N.C.
3. V+
4. N.C.
5. ON/OFF
6. GND
7. N.C.
8. IN-

Exposed PAD on backside connect to GND.

PIN FUNCTION
1. SW
2. N.C.
3. V+
4. N.C.
5. ON/OFF
6. GND
7. N.C.
8. IN-

BLOCK DIAGRAM

- SLOPE COMP.
- CURRENT SENSE
- OCP
- Buffer
- TSD
- PWM
- ER AMP
- Soft Start
- Vref 0.8V
- SW
- IN-
- V+
- 450kΩ
- ON/OFF
  High: ON
  Low: OFF
  (Standby)
- Standby ON/OFF
- Low Frequency Control
- OSC
- S O R
- GND
- New Japan Radio Co., Ltd.

Ver.2013-03-21
<table>
<thead>
<tr>
<th>PARAMETER</th>
<th>SYMBOL</th>
<th>MAXIMUM RATINGS</th>
<th>UNIT</th>
</tr>
</thead>
<tbody>
<tr>
<td>Supply Voltage</td>
<td>V⁺</td>
<td>+45</td>
<td>V</td>
</tr>
<tr>
<td>V⁺ - SW pin Voltage</td>
<td>V⁺,V⁻</td>
<td>+45</td>
<td>V</td>
</tr>
<tr>
<td>IN- pin Voltage</td>
<td>V(IN-)</td>
<td>-0.3 to +6</td>
<td>V</td>
</tr>
<tr>
<td>ON/OFF pin Voltage</td>
<td>V_ON/OFF</td>
<td>+45</td>
<td>V</td>
</tr>
<tr>
<td>Power Dissipation</td>
<td>P_D</td>
<td>625 (*1)</td>
<td>mW</td>
</tr>
<tr>
<td></td>
<td></td>
<td>2,400 (*2)</td>
<td></td>
</tr>
<tr>
<td></td>
<td></td>
<td>600 (*3)</td>
<td></td>
</tr>
<tr>
<td></td>
<td></td>
<td>1,800 (*4)</td>
<td></td>
</tr>
<tr>
<td>Junction Temperature Range</td>
<td>T_j</td>
<td>-40 to +150</td>
<td>°C</td>
</tr>
<tr>
<td>Operating Temperature Range</td>
<td>T_opr</td>
<td>-40 to +85</td>
<td>°C</td>
</tr>
<tr>
<td>Storage Temperature Range</td>
<td>T_stg</td>
<td>-40 to +150</td>
<td>°C</td>
</tr>
</tbody>
</table>

(*1): Mounted on glass epoxy board. (76.2×114.3×1.6mm: based on EIA/JEDEC standard size, 2Layers, Cu area 100mm²)
(*2): Mounted on glass epoxy board. (76.2×114.3×1.6mm: based on EIA/JEDEC standard, 4Layers)
(For 4Layers: Applying 74.2×74.2mm inner Cu area and a thermal via hall to a board based on JEDEC standard JESD51-5)

(*3): Mounted on glass epoxy board. (101.5×114.5×1.6mm: based on EIA/JEDEC standard, 2Layers FR-4, with Exposed Pad)
(*4): Mounted on glass epoxy board. (101.5×114.5×1.6mm: based on EIA/JEDEC standard, 4Layers FR-4, with Exposed Pad)
(For 4Layers: Applying 99.5×99.5mm inner Cu area and a thermal via hole to a board based on JEDEC standard JESD51-5)

<table>
<thead>
<tr>
<th>PARAMETER</th>
<th>SYMBOL</th>
<th>MIN.</th>
<th>TYP.</th>
<th>MAX.</th>
<th>UNIT</th>
</tr>
</thead>
<tbody>
<tr>
<td>Supply Voltage</td>
<td>V⁺</td>
<td>4.6</td>
<td>–</td>
<td>40</td>
<td>V</td>
</tr>
</tbody>
</table>
# ELECTRICAL CHARACTERISTICS
(Unless otherwise noted, $V^+=V_{ON/OFF}=12V$, $T_a=25^\circ C$)

<table>
<thead>
<tr>
<th>PARAMETER</th>
<th>SYMBOL</th>
<th>TEST CONDITION</th>
<th>MIN.</th>
<th>TYP.</th>
<th>MAX.</th>
<th>UNIT</th>
</tr>
</thead>
<tbody>
<tr>
<td><strong>Under Voltage Lockout Block</strong></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>ON Threshold Voltage</td>
<td>$V_{T_{ON}}$</td>
<td>$V^+=L\rightarrow H$</td>
<td>4.3</td>
<td>4.45</td>
<td>4.6</td>
<td>V</td>
</tr>
<tr>
<td>OFF Threshold Voltage</td>
<td>$V_{T_{OFF}}$</td>
<td>$V^+=H\rightarrow L$</td>
<td>4.2</td>
<td>4.35</td>
<td>4.5</td>
<td>V</td>
</tr>
<tr>
<td>Hysteresis Voltage</td>
<td>$V_{HYS}$</td>
<td></td>
<td>70</td>
<td>100</td>
<td>–</td>
<td>mV</td>
</tr>
</tbody>
</table>

| **Soft Start Block** | | | | | | |
| Soft Start Time | $T_{SS}$ | $V_B=0.75V$ | 2 | 4 | 8 | ms |

| **Oscillator Block** | | | | | | |
| Oscillation Frequency | $f_{OSC}$ | A version, $V_{IN}=0.7V$ | 900 | 1,000 | 1,100 | kHz |
| Oscillation Frequency (Low Frequency Control) | $f_{OSC\_LOW}$ | $V_{IN}=0.4V$ | – | 370 | – | kHz |
| Oscillation Frequency deviation (Supply voltage) | $f_{DV}$ | $V^+=4.6$ to $40V$ | – | 1 | – | % |
| Oscillation Frequency deviation (Temperature) | $f_{DT}$ | $T_a=-40^\circ C$ to $+85^\circ C$ | – | 5 | – | % |

| **Error Amplifier Block** | | | | | | |
| Reference Voltage | $V_B$ | | -1.0% | 0.8 | +1.0% | V |
| Input Bias Current | $I_B$ | | -0.1 | – | +0.1 | µA |

| **PWM Compare Block** | | | | | | |
| Maximum Duty Cycle | $M_{MAX\_DUTY}$ | $V_{IN}=0.7V$ | 85 | 90 | – | % |
| Minimum ON time | $t_{ON\_min}$ | | – | 140 | 180 | ns |

| **Over Current Protection Block** | | | | | | |
| Cool Down Time | $t_{COOL}$ | | – | 8 | – | ms |

| **Output Block** | | | | | | |
| Output ON Resistance | $R_{ON}$ | $I_{SW}=1A$ | – | 0.45 | 0.75 | Ω |
| Switching Current Limit | $I_{LIM}$ | | 1.4 | 1.9 | 2.4 | A |
| SW Leak Current | $I_{LEAK}$ | $V_{ON/OFF}=0V, V^+=45V, V_{SW}=0V$ | – | – | 1 | µA |

| **ON/OFF Block** | | | | | | |
| ON Control Voltage | $V_{ON}$ | $V_{ON/OFF}=L\rightarrow H$ | 1.6 | – | $V^+$ | V |
| OFF Control Voltage | $V_{OFF}$ | $V_{ON/OFF}=H\rightarrow L$ | 0 | – | 0.5 | V |
| Pull-down Resistance | $R_{PD}$ | | – | 450 | – | kΩ |

| **General Characteristics** | | | | | | |
| Quiescent Current | $I_{Q}$ | $R_L$=no load, $V_{IN}=0.7V$ | – | 3.9 | 4.4 | mA |
| Standby Current | $I_{Q\_STB}$ | $V_{ON/OFF}=0V$ | – | – | 1 | µA |
TYPICAL APPLICATIONS

V\textsuperscript{+} \quad \text{SW}
NJW4153
ON/OFF \quad \text{GND} \quad \text{IN-}
ON/OFF
High: ON
Low: OFF
(Standby)

\begin{align*}
V_{\text{IN}} & \quad \text{C}_{\text{IN}} \\
\text{VIN} & \quad \text{CIN} \\
\text{VOUT} & \quad \text{R1} \\
\end{align*}

L \quad \text{SBD} \quad \text{C}_{\text{OUT}} \quad \text{C}_{\text{FB}} \quad \text{R2}
TYPICAL CHARACTERISTICS

Oscillation Frequency vs. Supply Voltage
(A ver., $V_{in}=0.7V$, $T_a=25^\circ C$)

Reference Voltage vs. Supply Voltage
($T_a=25^\circ C$)

Quiescent Current vs. Supply Voltage
($R_{L}=no\ load, V_{in}=0.7V$, $T_a=25^\circ C$)
TYPICAL CHARACTERISTICS

Oscillation Frequency vs Temperature
(A ver., $V^+ = 12V$, $V_{IN} = 0.7V$)

Reference Voltage vs. Temperature
($V^+ = 12V$)

Switching Current Limit vs. Temperature

Output ON Resistance vs. Temperature
($I_{SW} = 1A$)

Under Voltage Lockout Voltage vs. Temperature
($V^+ = 12V$, $V_{IN} = 0.75V$)

Soft Start Time vs. Temperature
($V^+ = 12V$, $V_{IN} = 0.75V$)
TYPICAL CHARACTERISTICS

Minimum ON Time vs. Temperature
(V^+=12V)

Quiescent Current vs. Temperature
(R_L=no load, V_IN=0.7V)

Standby Current vs. Temperature
(V_{ON/OFF}=0V)

Switching Leak Current vs. Temperature
(V^+=45V, V_{ON/OFF}=0V, V_{SW}=0V)

Maximum Duty Cycle vs. Temperature
(V^+=12V, V_IN=0.7V)

Minimum ON Time, \( t_{ON-min} \) [ns]

Quiescent Current, \( I_{DD} \) (mA)

Standby Current, \( I_{DD-STB} \) (µA)

Switching Leak Current, \( I_{LEAK} \) (µA)

Maximum Duty Cycle, MAXDUTY (%)
## PIN DESCRIPTIONS

<table>
<thead>
<tr>
<th>PIN NAME</th>
<th>PIN NUMBER</th>
<th>FUNCTION</th>
</tr>
</thead>
<tbody>
<tr>
<td>ON/OFF</td>
<td>1</td>
<td>5</td>
</tr>
<tr>
<td></td>
<td>SOT-89-5</td>
<td>ESON8</td>
</tr>
</tbody>
</table>
|          | ON/OFF Control pin  
The ON/OFF pin internally pulls down with 450kΩ. Normal Operation at the time of High Level. Standby Mode at the time of Low Level or OPEN. |
| GND      | 2          | 6        |
|          |            |          |
| IN-      | 3          | 8        |
|          |            |          |
| SW       | 4          | 1        |
|          |            |          |
| V+       | 5          | 3        |
|          |            |          |
| N.C.     | –          | 2, 4, 7  |
| Exposed PAD | –          | –        |
|          |            | Connect to GND (Only ESOP8 PKG) |

Technical Information

NJW4153 Application Manual

New Japan Radio Co., Ltd.
Description of Block Features

1. Basic Functions / Features

- Error Amplifier Section (ER·AMP)
  0.8V±1% precise reference voltage is connected to the non-inverted input of this section.
  To set the output voltage, connects converter's output to inverted input of this section (IN- pin). If requires output voltage over 0.8V, inserts resistor divider.
  Because the optimized compensation circuit is built-in, the application circuit can be composed of minimum external parts.

- PWM Comparator Section (PWM), Oscillation Circuit Section (OSC)
  The NJW4153 uses a constant frequency, current mode step down architecture. The oscillation frequency is 1,000kHz (typ.) at A version. The PWM signal is output by feedback of output voltage and slope compensation switching current at the PWM comparator block.
  The maximum duty ratio is 90% (typ.).
  The minimum ON time is limited to 140nsec (typ.).
  The buck converter of ON time is decided the following formula.

\[
\text{ton} = \frac{V_{\text{OUT}}}{V_{\text{IN}} \times f_{\text{OSC}}} [\text{s}]
\]

\(V_{\text{IN}}\) shows input voltage and \(V_{\text{OUT}}\) shows output voltage.
  When the ON time becomes below \(t_{\text{ON-min}}\), in order to maintain output voltage at a stable state, change of duty or pulse skip operation may be performed.

- Power MOSFET (SW Output Section)
  The power is stored in the inductor by the switch operation of built-in power MOSFET. The output current is limited to 1.4A (min.) the overcurrent protection function. In case of step-down converter, the forward direction bias voltage is generated with inductance current that flows into the external regenerative diode when MOSFET is turned off.
  The SW pin allows voltage between the V+ pin and the SW pin up to +45V. However, you should use an Schottky diode that has low saturation voltage.

- Power Supply, GND pin (V+ and GND)
  In line with switching element drive, current flows into the IC according to frequency. If the power supply impedance provided to the power supply circuit is high, it will not be possible to take advantage of IC performance due to input voltage fluctuation. Therefore insert a bypass capacitor close to the V+ pin – the GND pin connection in order to lower high frequency impedance.
2. Additional and Protection Functions / Features

- Under Voltage Lockout (UVLO)

The UVLO circuit operating is released above $V^+ = 4.45V$(typ.) and IC operation starts. When power supply voltage is low, IC does not operate because the UVLO circuit operates. There is $100mV$(typ.) width hysteresis voltage at rise and decay of power supply voltage. Hysteresis prevents the malfunction at the time of UVLO operating and releasing.

- Soft Start Function (Soft Start)

The output voltage of the converter gradually rises to a set value by the soft start function. The soft start time is 4ms (typ.). It is defined with the time of the error amplifier reference voltage becoming from 0V to 0.75V. The soft start circuit operates after the release UVLO and/or recovery from thermal shutdown. The operating frequency is controlled with a low frequency 370kHz, until voltage or the IN- pin becomes approximately 0.65V.

![Startup Timing Chart](image)

Fig. 1. Startup Timing Chart
Description of Block Features (Continued)

- Over Current Protection Circuit (OCP)
  NJW4153 contains overcurrent protection circuit of hiccup architecture. The overcurrent protection circuit of hiccup architecture is able to decrease heat generation at the overload.

The NJW4153 output returns automatically along with release from the over current condition. At when the switching current becomes \( I_{\text{ILIM}} \) or more, the overcurrent protection circuit is stopped the MOSFET output. The switching output holds low level down to next pulse output at OCP operating.

When IN- pin voltage becomes 0.5V or less, it operates with 370kHz (typ.). At the same time starts pulse counting, and stops the switching operation when the overcurrent detection continues approx 1ms.

After NJW4153 switching operation was stopped, it restarts by soft start function after the cool down time of approx 8ms (typ.).

**Fig. 2. Timing Chart at Over Current Detection**

- Thermal Shutdown Function (TSD)
  When Junction temperature of the NJW4153 exceeds the 165°C*, internal thermal shutdown circuit function stops SW function. When junction temperature decreases to 150°C* or less, SW operation returns with soft start operation.

  The purpose of this function is to prevent malfunctioning of IC at the high junction temperature. Therefore it is not something that urges positive use. You should make sure to operate within the junction temperature range rated (150°C). (* Design value)

- ON/OFF Function
  The NJW4153 stops the operating and becomes standby status when the ON/OFF pin becomes less than 0.5V. The ON/OFF pin internally pulls down with 450kΩ, therefore the NJW4153 becomes standby mode when the ON/OFF pin is OPEN. You should connect this pin to \( V^+ \) when you do not use standby function.
Application Information

Inductors

Because a large current flows to the inductor, you should select the inductor with the large current capacity not to saturate. Optimized inductor value is determined by the input voltage and output voltage.

The Optimized inductor value: (It is a reference value.)

\[ V_{IN}=12V \rightarrow V_{OUT}=5.0V \quad : \quad L \leq 10\mu H \]
\[ V_{IN}=24V \rightarrow V_{OUT}=5.0V \quad : \quad L \leq 10\mu H \]

You should set the inductor as a guide from above mentioned value to half value. Reducing L decreases the size of the inductor. However a peak current increases and adversely affects the efficiency. (Fig. 3.)

Moreover, you should be aware that the output current is limited because it becomes easy to operating to the overcurrent limit.

The peak current is decided the following formula.

\[ \Delta I_L = \frac{(V_{IN} - V_{OUT}) \times V_{OUT}}{L \times V_{IN} \times f_{OSC}} \quad [A] \]

\[ Ipk = I_{OUT} + \frac{\Delta I_L}{2} \quad [A] \]

Fig. 3. Inductor Current State Transition (Continuous Conduction Mode)
**Catch Diode**

When the switch element is in OFF cycle, power stored in the inductor flows via the catch diode to the output capacitor. Therefore during each cycle current flows to the diode in response to load current. Because diode’s forward saturation voltage and current accumulation cause power loss, a Schottky Barrier Diode (SBD), which has a low forward saturation voltage, is ideal.

An SBD also has a short reverse recovery time. If the reverse recovery time is long, through current flows when the switching transistor transitions from OFF cycle to ON cycle. This current may lower efficiency and affect such factors as noise generation.

**Input Capacitor**

Transient current flows into the input section of a switching regulator responsive to frequency. If the power supply impedance provided to the power supply circuit is large, it will not be possible to take advantage of the NJW4153 performance due to input voltage fluctuation. Therefore insert an input capacitor as close to the MOSFET as possible. A ceramic capacitor is the optimal for input capacitor.

The effective input current can be expressed by the following formula.

\[
I_{RMS} = I_{OUT} \times \sqrt{V_{OUT} \times \left( \frac{V_{IN} - V_{OUT}}{V_{IN}} \right)} \quad [A]
\]

In the above formula, the maximum current is obtained when \( V_{IN} = 2 \times V_{OUT} \), and the result in this case is \( I_{RMS} = I_{OUT (MAX)} \div 2 \).

When selecting the input capacitor, carry out an evaluation based on the application, and use a capacitor that has adequate margin.

**Output Capacitor**

An output capacitor stores power from the inductor, and stabilizes voltage provided to the output. Because NJW4153 corresponds to the output capacitor of low ESR, the ceramic capacitor is the optimal for compensation.

The Optimized capacitor value: (It is a reference value.)

\[ V_{OUT} = 5.0V \quad : \quad C_{OUT} \geq 22\mu F \]

In addition, you should consider varied characteristics of capacitor (a frequency characteristic, a temperature characteristic, a DC bias characteristic and so on) and unevenness peculiar to a capacitor supplier enough. Therefore when selecting a capacitors, you should confirm the characteristics with supplier datasheets.

When selecting an output capacitor, you must consider Equivalent Series Resistance (ESR) characteristics, ripple current, and breakdown voltage.

The output ripple noise can be expressed by the following formula.

\[ V_{ripple(p-p)} = ESR \times \Delta I_L \quad [V] \]

The effective ripple current that flows in a capacitor \( (I_{rms}) \) is obtained by the following equation.

\[ I_{rms} = \frac{\Delta I_L}{2\sqrt{3}} \quad [Arms] \]
■ Application Information (Continued)

- Setting Output Voltage, Compensation Capacitor

  The output voltage $V_{OUT}$ is determined by the relative resistances of $R_1$, $R_2$. The current that flows in $R_1$, $R_2$ must be a value that can ignore the bias current that flows in ER AMP:

  $$V_{OUT} = \left( \frac{R_2}{R_1} + 1 \right) \times V_B \text{ [V]}$$

  The zero points are formed with $R_2$ and $C_{FB}$, and it makes for the phase compensation of NJW4153. The zero point is shown the following formula.

  $$f_{Z1} = \frac{1}{2 \times \pi \times R_2 \times C_{FB}} \text{ [Hz]}$$

  You should set the zero point as a guide from $30kHz$ to $50kHz$. 
Application Information (Continued)

• Board Layout

In the switching regulator application, because the current flow corresponds to the oscillation frequency, the substrate (PCB) layout becomes an important.

You should attempt the transition voltage decrease by making a current loop area minimize as much as possible. Therefore, you should make a current flowing line thick and short as much as possible. Fig. 4. shows a current loop at step-down converter. Especially, should lay out high priority the loop of $C_{\text{IN}}$-SW-SBD that occurs rapid current change in the switching. It is effective in reducing noise spikes caused by parasitic inductance.

Fig. 4. Current Loop at Buck Converter

Concerning the GND line, it is preferred to separate the power system and the signal system, and use single ground point.

The voltage sensing feedback line should be as far away as possible from the inductance. Because this line has high impedance, it is laid out to avoid the influence noise caused by flux leaked from the inductance.

Fig. 5. shows example of wiring at buck converter. Fig. 6 shows the PCB layout example.

Fig. 5. Board Layout at Buck Converter

Separate Digital(Signal) GND from Power GND

Because IN- pin is high impedance, the voltage detection resistance: $R_1/R_2$ is put as much as possible near IC(IN-). To avoid the influence of the voltage drop, the output voltage should be detected near the load.
Connect Signal GND line and Power GND line on backside pattern

Fig. 6. Layout Example (upper view)
Calculation of Package Power

A lot of the power consumption of buck converter occurs from the internal switching element (Power MOSFET). Power consumption of NJW4153 is roughly estimated as follows.

\[
\begin{align*}
\text{Input Power:} & \quad P_{IN} = V_{IN} \times I_{IN} \quad [W] \\
\text{Output Power:} & \quad P_{OUT} = V_{OUT} \times I_{OUT} \quad [W] \\
\text{Diode Loss:} & \quad P_{DIODE} = V_F \times I_{L(\text{avg})} \times \text{OFF duty} \quad [W] \\
\text{NJW4153 Power Consumption:} & \quad P_{\text{LOSS}} = P_{IN} - P_{OUT} - P_{DIODE} \quad [W]
\end{align*}
\]

Where:
- \( V_{IN} \): Input Voltage for Converter
- \( I_{IN} \): Input Current for Converter
- \( V_{OUT} \): Output Voltage of Converter
- \( I_{OUT} \): Output Current of Converter
- \( V_F \): Diode's Forward Saturation Voltage
- \( I_{L(\text{avg})} \): Inductor Average Current
- \( \text{OFF duty} \): Switch OFF Duty

Efficiency (\( \eta \)) is calculated as follows.

\[
\eta = \left( \frac{P_{OUT}}{P_{IN}} \right) \times 100 \%\]

You should consider temperature derating to the calculated power consumption: \( P_D \).

You should design power consumption in rated range referring to the power dissipation vs. ambient temperature characteristics (Fig. 7).

Fig. 7. Power Dissipation vs. Ambient Temperature Characteristics

(*5): Mounted on glass epoxy board. (76.2×114.3×1.6mm: based on EIA/JDEC standard size, 2Layers, Cu area 100mm²)
(*6): Mounted on glass epoxy board. (76.2×114.3×1.6mm: based on EIA/JDEC standard, 4Layers)
(For 4Layers: Applying 74.2×74.2mm inner Cu area and a thermal via hall to a board based on JEDEC standard JESD51-5)

(*7): Mounted on glass epoxy board. (101.5×114.5×1.6mm: based on EIA/JEDEC standard, 2Layers FR-4, with Exposed Pad)
(*8): Mounted on glass epoxy board. (101.5×114.5×1.6mm: based on EIA/JEDEC standard, 4Layers FR-4, with Exposed Pad)
(For 4Layers: Applying 99.5×99.5mm inner Cu area and a thermal via hole to a board based on JEDEC standard JESD51-5)
Application Design Examples

Busk Converter Application Circuit

IC: NJW4153U2-A
Input Voltage: VIN=12V, 24V
Output Voltage: VOUT=5V
Output Current: IOUT=1A
Oscillation frequency: fosc=1MHz

Reference Qty. Part Number Description Manufacturer

IC 1 NJW4153U2-A Internal 1A MOSFET SW.REG. IC New JRC
L 1 CDRH8D28HPNP-100N Inductor 10μH, 3.4A(Ta=25°C) / 2.5A(Ta=100°C) Sumida
SBD 1 CMS16 Schottky Diode 40V, 3A Toshiba
CIN 1 UMK325BJ106MM Ceramic Capacitor 3225 10μF, 50V, X5R Taiyo Yuden
COUT 1 GRM32EB31E226KE15 Ceramic Capacitor 3225 22μF, 25V, B Murata
CFB 1 22pF Ceramic Capacitor 1608 22pF, 50V, CH Std.
R1 1 30kΩ Resistor 1608 30kΩ, ±1%, 0.1W Std.
R2 1 160kΩ Resistor 1608 160kΩ, ±1%, 0.1W Std.
Technical Information

**Application Characteristics:** NJW4153U2-A

- At $V_{OUT}=5.0\,\text{V}$ setting ($R_1=30k\Omega$, $R_2=160k\Omega$)

Efficiency vs. Output Current
(A ver., $V_{OUT}=5\,\text{V}$, $T_a=25^\circ\text{C}$)

- At $V_{OUT}=3.3\,\text{V}$ setting ($R_1=47k\Omega$, $R_2=150k\Omega$)

Efficiency vs. Output Current
(A ver., $V_{OUT}=3.3\,\text{V}$, $T_a=25^\circ\text{C}$)
Inverting Converter Application Circuit

\[ V_{IN} = 15V \]

- SW
- GND
- ON/OFF

\[ C_C \]
- 0.47µF/50V

\[ L \]
- 10µH/3.4A

\[ C_{IN} \]
- 10µF/50V

\[ C_{FB} \]
- Optional

\[ C_{OUT} \]
- 22µF/25V

\[ R1 \]
- 910kΩ

\[ R2 \]
- 51kΩ

\[ V_{OUT} = -15V \]

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### Technical Information

#### Efficiency vs. Output Current

**Efficiency vs. Output Current**

(A ver., \( V_{IN}=15V \), \( V_{OUT}=-15V \), \( Ta=25°C \))

**Output Voltage vs. Output Current**

(A ver., \( V_{IN}=15V \), \( Ta=25°C \))

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### Reference Component List

<table>
<thead>
<tr>
<th>Reference</th>
<th>Qty</th>
<th>Part Number</th>
<th>Description</th>
<th>Manufacturer</th>
</tr>
</thead>
<tbody>
<tr>
<td>IC</td>
<td>1</td>
<td>NJW4153U2-A</td>
<td>Internal 1A MOSFET SW.REG. IC</td>
<td>New JRC</td>
</tr>
<tr>
<td>L</td>
<td>1</td>
<td>CDRH8D28HPNP-100N</td>
<td>Inductor 10µH, 3.4A(Ta=25°C) / 2.5A(Ta=100°C)</td>
<td>Sumida</td>
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<tr>
<td>SBD</td>
<td>1</td>
<td>CMS16</td>
<td>Schottky Diode 40V, 3A</td>
<td>Toshiba</td>
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<tr>
<td>C(_{IN})</td>
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<td>UMK325BJ106MM</td>
<td>Ceramic Capacitor 3225 10µF, 50V, X5R</td>
<td>Taiyo Yuden</td>
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<tr>
<td>C(_{OUT})</td>
<td>1</td>
<td>GRM32EB31E226KE15</td>
<td>Ceramic Capacitor 3225 22µF, 25V, B</td>
<td>Murata</td>
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<td>C(_{C})</td>
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<td>GRM21BB31H474KA87</td>
<td>Ceramic Capacitor 2012 0.47µF, 50V, B</td>
<td>Murata</td>
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<tr>
<td>C(_{FB})</td>
<td>0</td>
<td>Optional</td>
<td>Optional</td>
<td>——</td>
</tr>
<tr>
<td>R1</td>
<td>1</td>
<td>910kΩ</td>
<td>Resistor 1608 910kΩ, ±1%, 0.1W</td>
<td>Std.</td>
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<tr>
<td>R2</td>
<td>1</td>
<td>51kΩ</td>
<td>Resistor 1608 51kΩ, ±1%, 0.1W</td>
<td>Std.</td>
</tr>
</tbody>
</table>
[CAUTION]

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