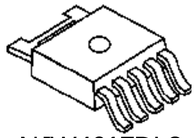
Constant Current LED Driver with PWM Dimming Control

■ GENERAL DESCRIPTION

The NJW4617 is a constant current LED driver with PWM dimming control. The output current can be set by external sensing resister, and the NJW4617 can set up to 500mA.
Because the withstand voltage of the output (LED) pin is 40V, it can series-connect the LED depending on forward voltage of the LED. The LED dimming control can be regulated via PWM duty cycle. It is suitable for back light, light source and so on.

■ PACKAGE OUTLINE

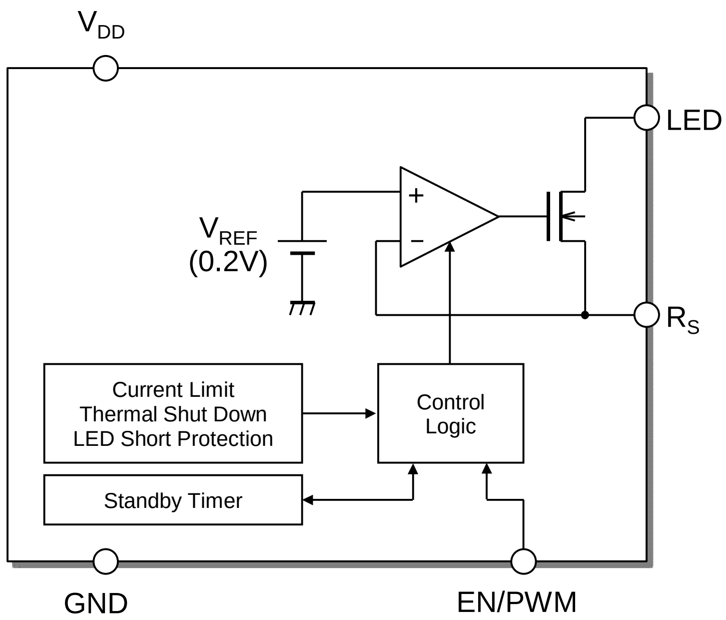


NJW4617DL3
(TO-252-5)

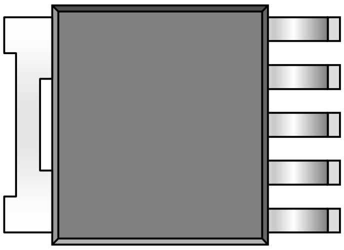
■ FEATURES

- Operating Voltage Range: 2.5V to 40V
- Output Voltage: V_{LED}=40V max.
- Output Current: I_{LED}=20mA to 500mA
- Output Current Accuracy: ±1.5%
- To 11 of White LED can be operated. (at LED Vf=3.4V)
- Quiescent Current: 450µA max.
- PWM Dimming Control and Enable Control (Common Pin)
- Over Current Protection
- Thermal Shutdown Protection
- LED Short Protection
- Package: TO-252-5

■ BLOCK DIAGRAM



■ PIN CONFIGURATION



■ PIN DESCRIPTIONS

Pin No.	Pin Name	I/O	Function
1	V_{DD}	-	Power supply pin
2	EN/PWM	I	Standby control pin and PWM signal input pin for dimming control. [At Standby control] Normal operation: High Level. Standby mode: Low Level. [At PWM signal input] The LED dimming control can be regulated by PWM duty cycle. In the case without dimming, I_{LED} becomes the current set by the external resistor (Rs) when High level is input to this pin. Note that this pin should not be open.
3	GND	-	Ground pin
4	R_S	O	Resistor connect pin of I_{LED} setting. The LED current can be set with connected resistor (R_S) between R_S pin and GND pin. R_S [Ω] = 0.2 [V] / I_{LED} [A]
5	LED	O	Constant current circuit output pin Connect cathode pin of LED.

■ ABSOLUTE MAXIMUM RATINGS (T_a = 25°C)

PARAMETER	SYMBOL	RATINGS	UNIT
Supply Voltage	V_{DD}	-0.3 to +45	V
Output Voltage	V_{LED}	-0.3 to +45	V
EN/PWM Pin Voltage	V_{ENPWM}	-0.3 to +45	V
Power Dissipation	P_D	1190 (*1) 3125 (*2)	mW
Junction Temperature Range	T_j	-40 to +150	°C
Operating Temperature Range	T_{opr}	-40 to +125	°C
Storage Temperature Range	T_{stg}	-50 to +150	°C

(*1) Mounted on glass epoxy board. (76.2 × 114.3 × 1.6mm: based on EIA/JEDEC standard size, 2Layers, Cu area 100mm^2)

(*2) Mounted on glass epoxy board. (76.2 × 114.3 × 1.6mm: based on EIA/JEDEC standard, 4Layers),

(For 4Layers: Applying 74.2 × 74.2mm inner Cu area and a thermal via hole to a board based on JEDEC standard JESD51-5)

■ RECOMMENDED OPERATING CONDITIONS

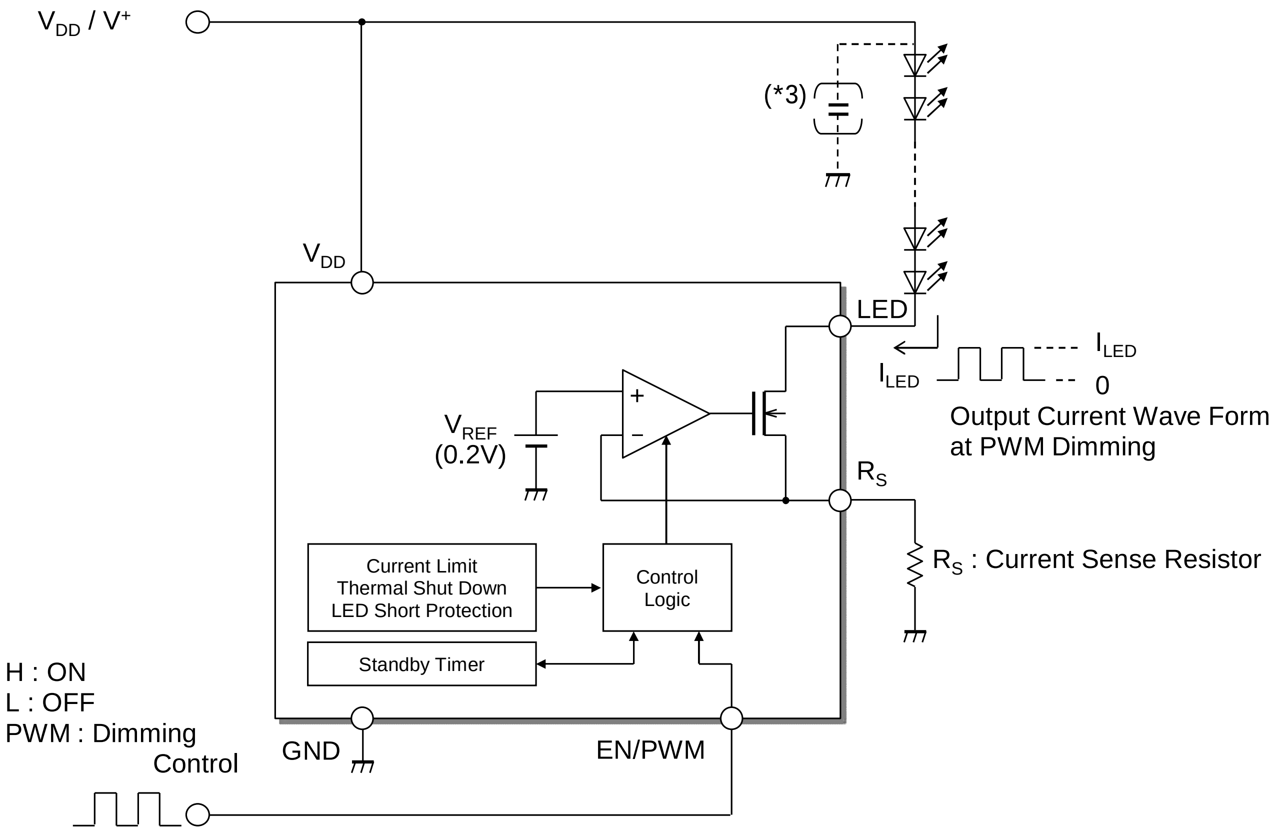
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNIT
Supply Voltage	V_{DD}		2.5	-	40	V
Output Current	I_{LED}		20	-	500	mA
Output Voltage	V_{LED}		-	-	40	V

■ ELECTRICAL CHARACTERISTICS

(Unless otherwise noted, V_{DD} = 12V, V_{LED} = 1V, R_S = 2Ω, V_{ENPWM} = V_{DD}, Ta = 25°C)

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	Unit
Quiescent Current	I_{DD}		-	330	450	µA
Quiescent Current at OFF State	I_{DD_OFF}	V_{ENPWM} = GND	-	-	0.1	µA
Output Current Accuracy	ΔI_{LED}		-1.5	-	+1.5	%
Output (LED) Pin Leak Current	I_{LEAK}	V_{ENPWM} = GND, V_{DD} = V_{LED} = 40V	-	-	0.1	µA
OFF Delay Time	t_{D_OFF}		10	25	45	ms
EN/PWM Pin ON Voltage1	$V_{ENPWM\_ON}1$	V_{DD} < 5V, I_{LED} = OFF → ON	0.7V_{DD}	-	V_{DD}	V
EN/PWM Pin ON Voltage2	$V_{ENPWM\_ON}2$	V_{DD} ≥ 5V, I_{LED} = OFF → ON	3.5	-	V_{DD}	V
EN/PWM Pin OFF Voltage	V_{ENPWM_OFF}	I_{LED} = ON → OFF	0	-	0.5	V
EN/PWM Pin Input Current	I_{ENPWM}		-	7	-	µA
R_S Pin Output Current	I_{OUT_RS}	LED = OPEN	-	4	-	µA
PWM Dimming ON Delay Time	t_{PWM_ON}	R_S = 0.4Ω, V_{ENPWM} = L → H	-	13	-	µs
PWM Dimming OFF Delay Time	t_{PWM_OFF}	R_S = 0.4Ω, V_{ENPWM} = H → L	-	1	-	µs
LED Short Protection Detect Voltage	V_{LED_SHORT}		18	21	24	V
Maximum Output Current	I_{LED_MAX}	R_S = 0Ω	500	850	-	mA

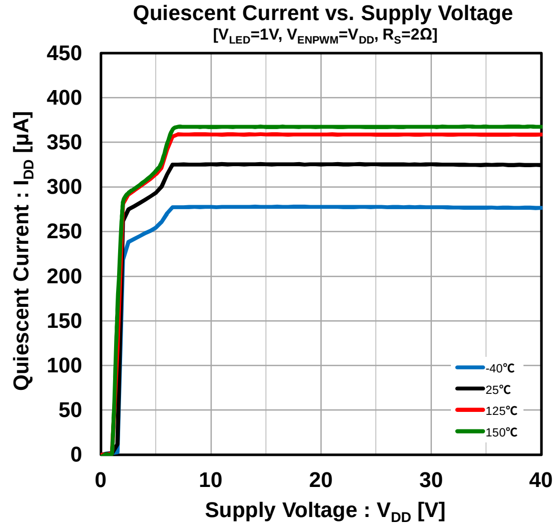
■ TYPICAL APPLICATION

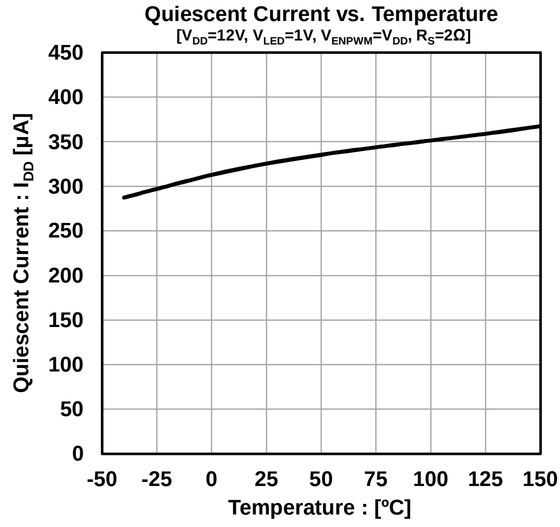


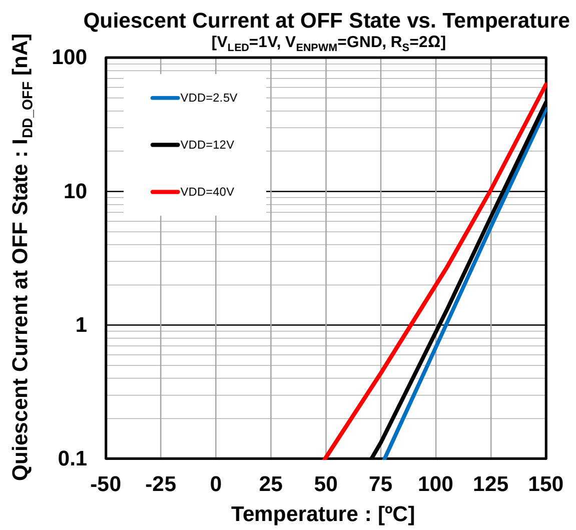
The R_s Resistance Setting formula: $R_S(\Omega) = \dfrac{0.2(V)}{I_{LED}(A)}$

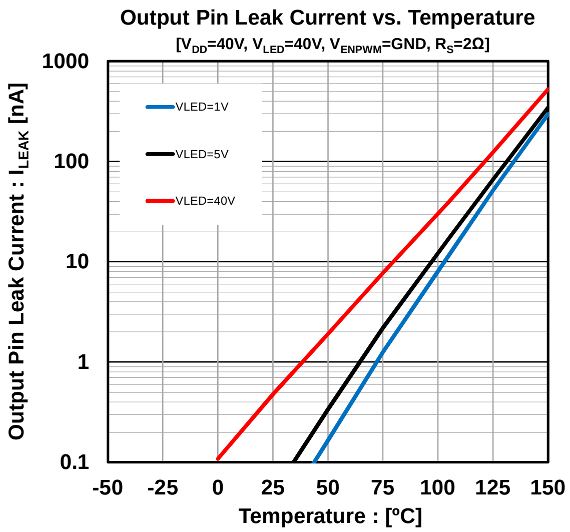
(*3) If the wiring from the power supply to the LED anode pin is long, the voltage may change due to the influence of the parasitic elements.
As the countermeasure, it should connect a decoupling capacitor as close to the LED as possible.

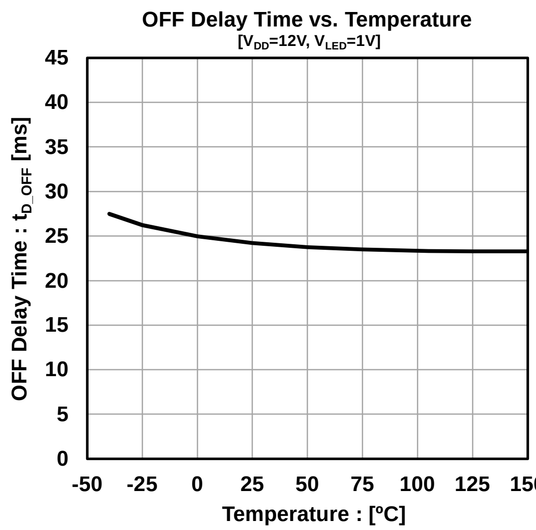
■ TYPICAL CHARACTERISTICS











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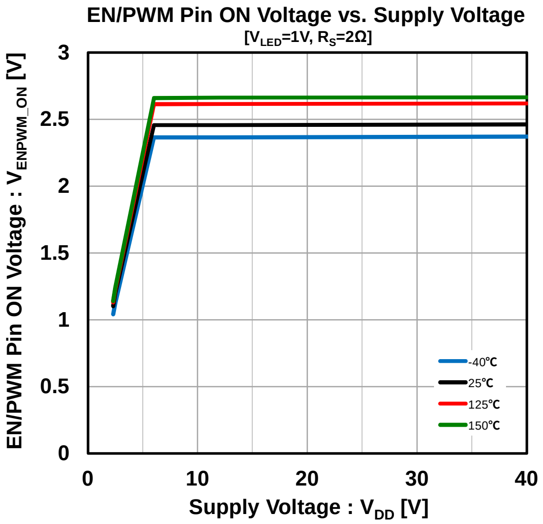

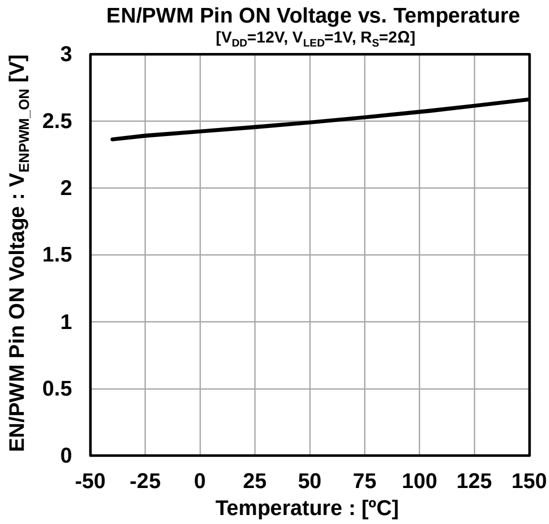

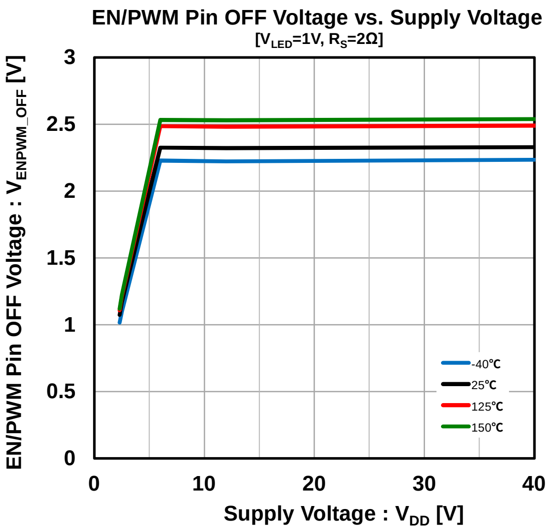

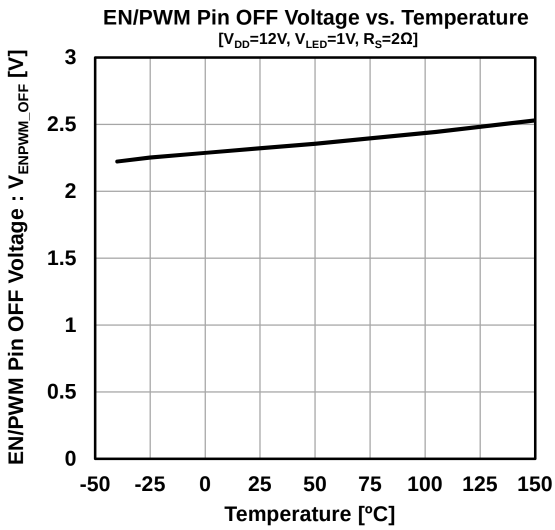

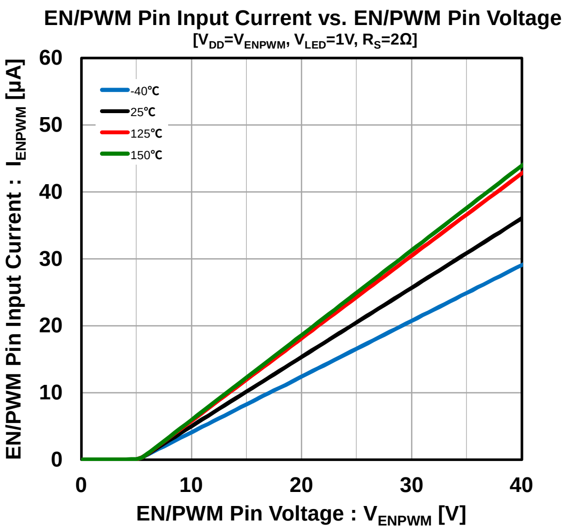

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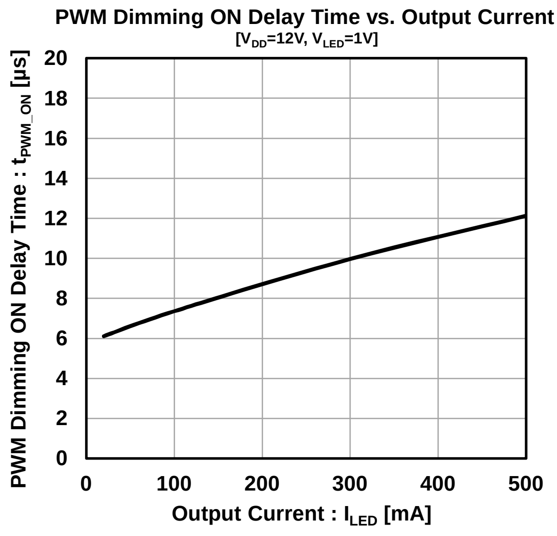

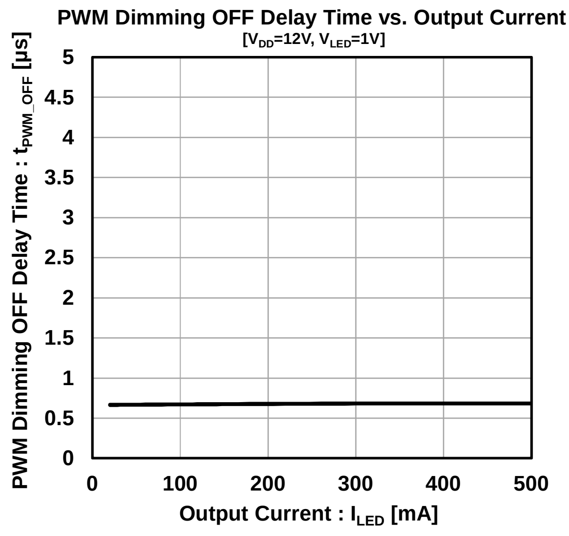

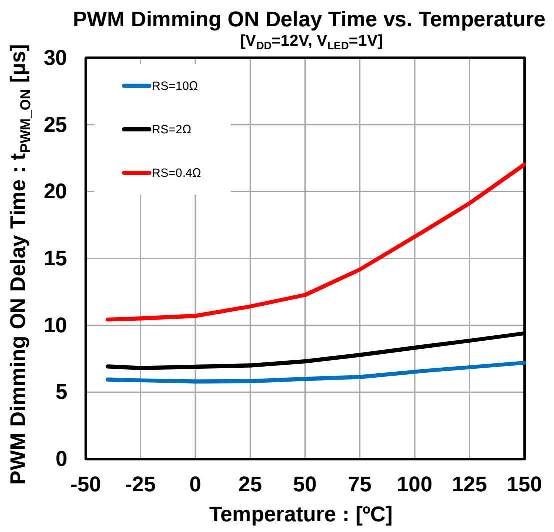

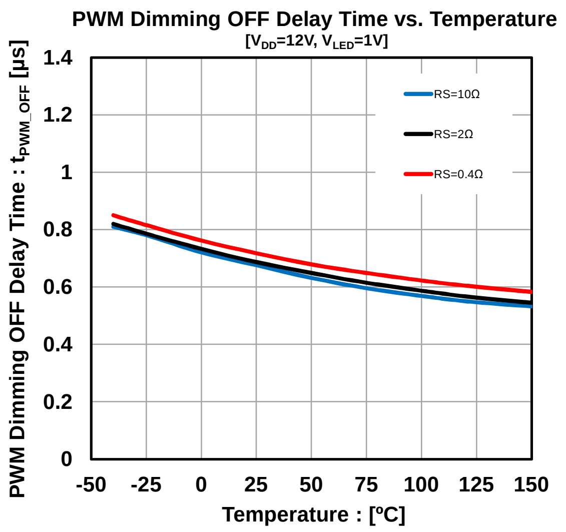

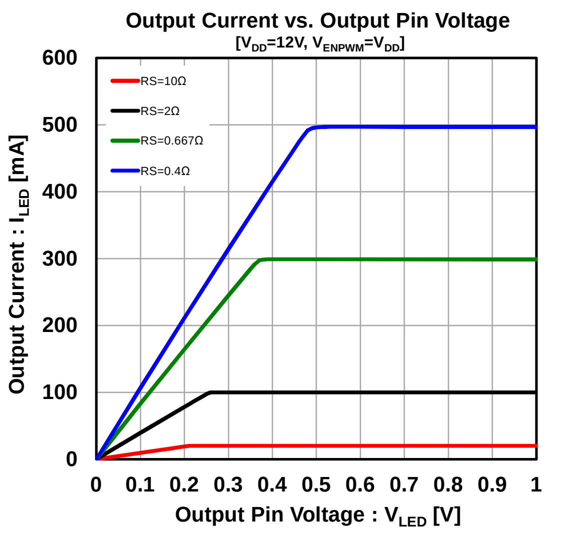

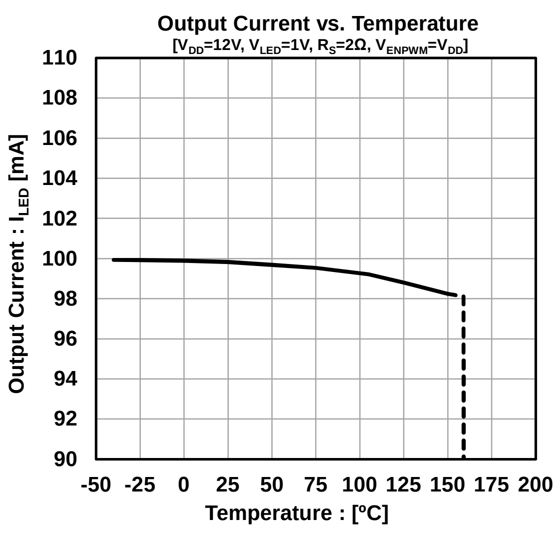

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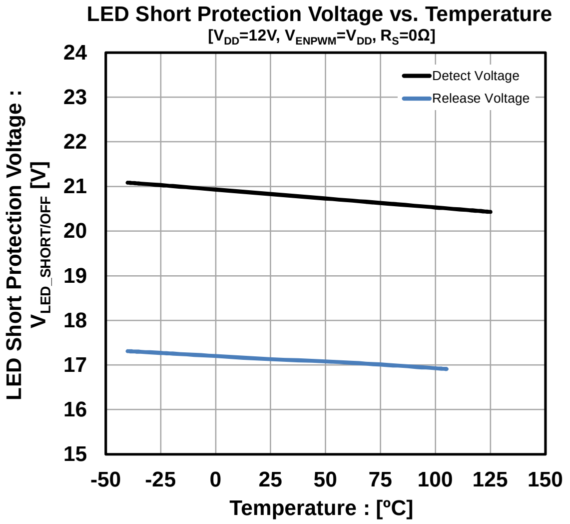
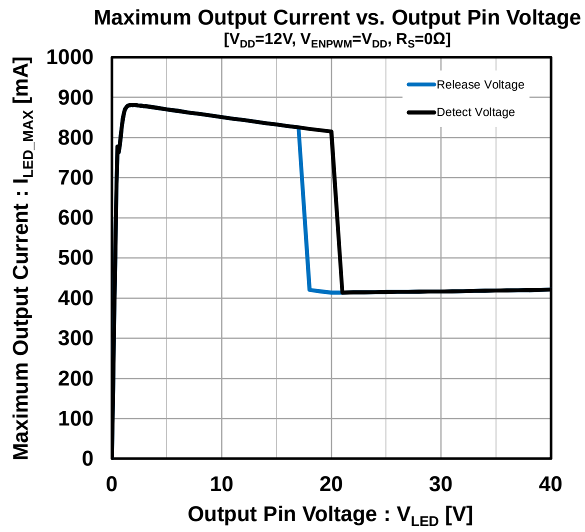
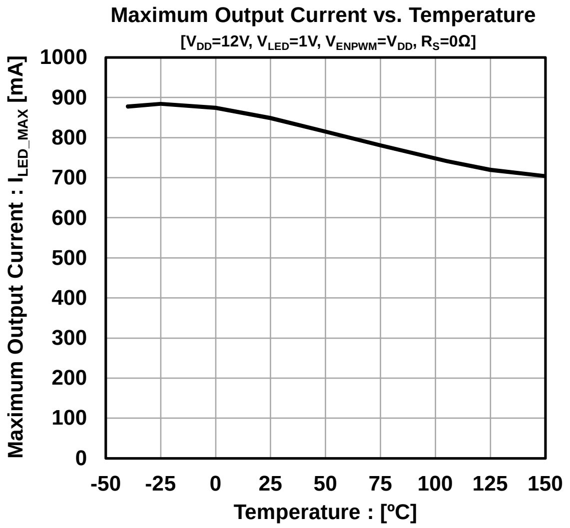
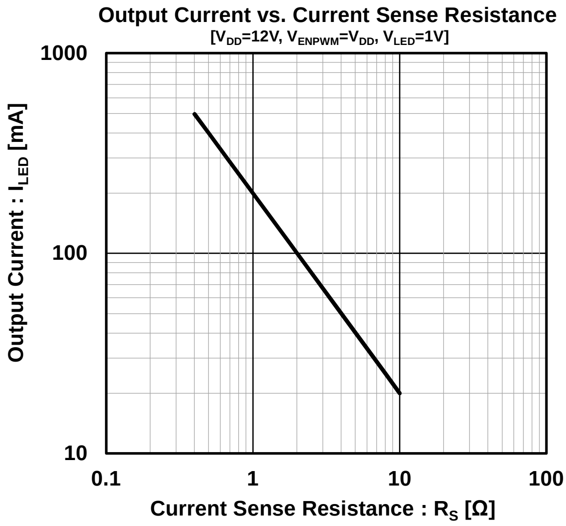






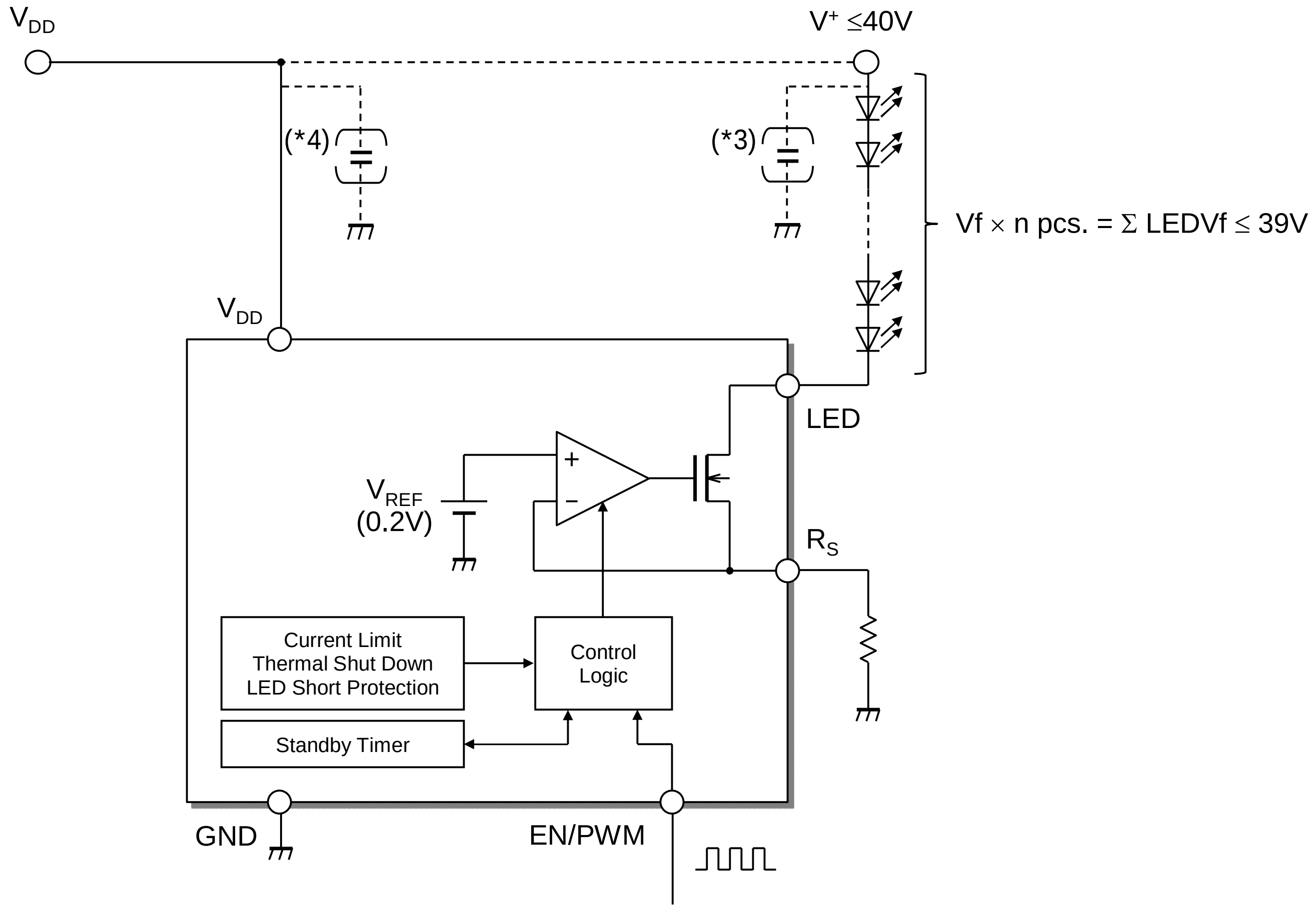


■ The number of LED series connection

It is necessary to drive LED that is the LED forward voltage (Vf) or more.
When the LED was series connected, the supply voltage should be input sum of LED Vf (ΣLED Vf) the series connected or more. In NJW4617, it is necessary as minimum V^+ that is ΣLED Vf + NJW4617 output voltage (V_{LED} = 1V). The maximum LED connected number that NJW4617 can drive is limited by the recommended output voltage maximum value (40V). Moreover, it should be used with ΣLED Vf within 39V that is subtracted the V_{LED} = 1V.

The table below shows maximum LED number at each Vf. (All LED Vf assumes ideally same)

LED Vf is up to 3.0V	▪▪▪	up to 13 lights
LED Vf is up to 3.2V	▪▪▪	up to 12 lights
LED Vf is up to 3.5V	▪▪▪	up to 11 lights
LED Vf is up to 3.9V	▪▪▪	up to 10 lights
LED Vf is up to 4.3V	▪▪▪	up to 9 lights

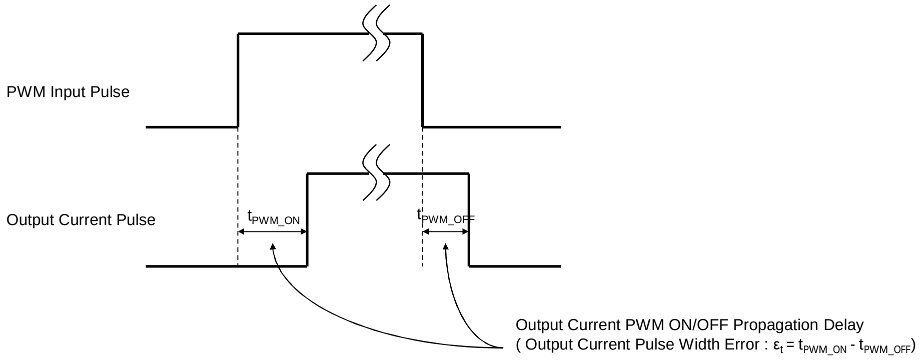


(*4) If the wiring from the power supply to the V_{DD} pin is long, the voltage may change due to the influence of the parasitic elements.
As the countermeasure, it should connect a decoupling capacitor as close to the V_{DD} pin as possible.

Application Manual

■ PWM input pulse and PWM dimming accuracy

The I_{LED} transient behavior corresponding to PWM input pulse has some delay at rise/fall time.



If enter a PWM signal with short pulse width, for the output current pulse width error becomes larger against the PWM input pulse width, it is incapable of accurate PWM dimming. The Output current pulse width error rate (ε_t) becomes the following value. ($\varepsilon_t = t_{PWM\_ON} - t_{PWM\_OFF}$)

ε_t = approx. 12µs (Output Current: I_{LED}=500mA,Ta=25°C typ. <Reference Value>)

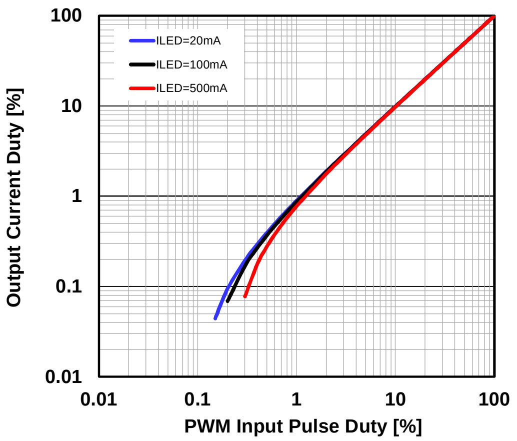
The actual value of the output current pulse width error rate can calculate by above error rate (ε_t) and the frequency and Duty of the PWM input pulse. (f_{PWM}: PWM input pulse frequency, D: PWM input pulse Duty)
Based on the allowable value of the output current pulse width error rate, you should determine the frequency and Duty of the PWM input pulse.

PWM Input pulse width = $D / 100 \times (1 / f_{PWM})$
Output Current pulse width = PWM Input pulse width - ε_t
Output Current pulse width error rate
= (Output Current pulse width - PWM Input pulse width) / PWM Input pulse width × 100
= $-\varepsilon_t$ / PWM Input pulse width × 100 [%]

【 Output current pulse width error rate calculation example:
operation with PWM input pulse frequency 200Hz and Duty1% 】

PWM Input pulse width = 1 [%] / 100 × (1 / 200[Hz]) = 50 [µs]
Output Current pulse width error rate = -12 [µs] / 50 [µs] × 100
= -24 [%]

Output Current Duty vs. PWM Input Pulse Duty



【 Measurement Conditions 】

Output Current (I_{LED})	20mA	(R_S= 10Ω)
	100mA	(R_S= 2Ω)
	500mA	(R_S= 0.4Ω)
Supply Voltage	12V	
Output (LED) pin	Input Voltage at 1V	
PWM Input Pulse	200Hz, 0 to 5V	
Output Current I_{LED} Pulse Width	The time is more than 90% of set current.	
Ambient Temperature	Ta=25ºC	

■ Protection Circuit

▪ Over Current Protection (Refer to Maximum Output Current vs. Output Pin Voltage)

This protection function limits the output current, when the R_S pin and GND pin was shorted. The limited current is dependence on output (LED) pin voltage.
When the output (LED) pin voltage is less than "LED Short Protection Detect Voltage", maximum output current is limited to approx. 850mA (output (LED) pin voltage=1V, Ta=25°C).
The output current returns to set current, when the short status is release.

▪ LED Short Protection (Refer to Maximum Output Current vs. Output Pin Voltage)

This protection function limits the output current, when the output (LED) pin rises as in LED shorten at output FET ON.
The output current is limited to approx. 400mA when the output (LED) pin voltage rose to approx. 21V.

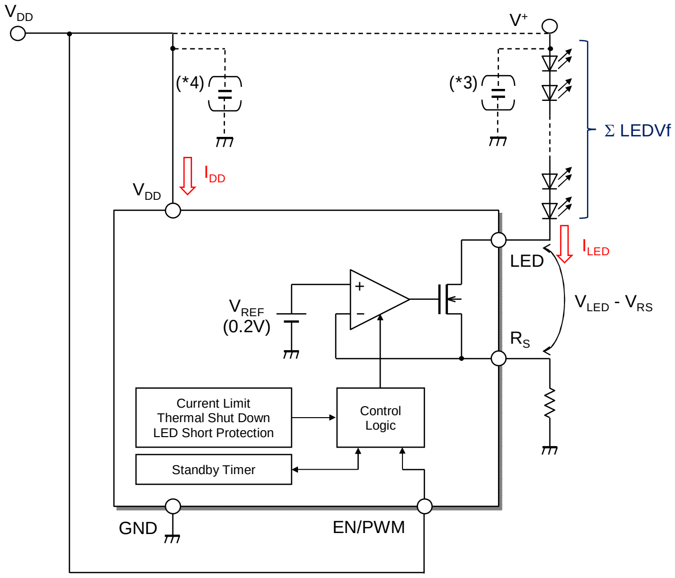
▪ Thermal Shutdown Function (Refer to Output Current vs. Temperature)

When junction temperature of the NJW4617 exceeds the 160°C*, internal thermal shutdown circuit function stops the device function. When junction temperature decreases to 140°C* or less, the device operation returns to normal operation.
The purpose of this function is to prevent malfunctioning of IC at the high junction temperature. Therefore it is not something that urges positive use. It should make sure to operate within the junction temperature range rated (≤+150°C).

*) Design value

■ The Loss of Constant Current Driver

The power consumption of the LED lighting circuit is classified as "the power consumption of the constant current driver" "the power consumption of the LED" and "the power consumption of the current sense resistor (R_S)".
The loss of constant current driver is caused mainly by quiescent current (I_{DD}) and output current (I_{LED}).
The power dissipation of the device can calculate by follow equation.



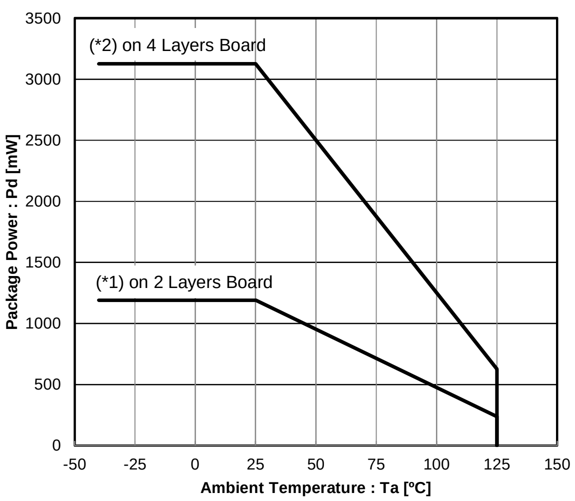
$$P_D \approx V_{DD} \times I_{DD} + (V_{LED} - V_{RS}) \times I_{LED}$$
$$= V_{DD} \times I_{DD} + (V^+ - \Sigma LED\ Vf - 0.2) \times I_{LED}\ [W]$$

Rs pin voltage (V_{RS}): 0.2V
Σ LED Vf represents the sum of the LED Vf of use.

e.g.)
$V_{DD} = V^+ = 12[V]$, $I_{DD}=330[\mu A]$, $\Sigma LED\ Vf = 9[V]$,
$I_{LED} = 400[mA]$

$$P_D \approx 12[V] \times 330[\mu A] + (12[V] - 9[V] - 0.2[V]) \times 400[mA]$$
$$\approx 1124[mW]$$

As shown in the above equation, the loss of constant current driver will increase in proportion to the voltage difference between the LED driving voltage V^+ and ΣLED Vf.
The device power dissipation must be below the power dissipation rate of the device package including thermal derating to ensure correct operation. It should set the LED operating Voltage (V^+) and output current (I_{LED}) with consideration of P_D.



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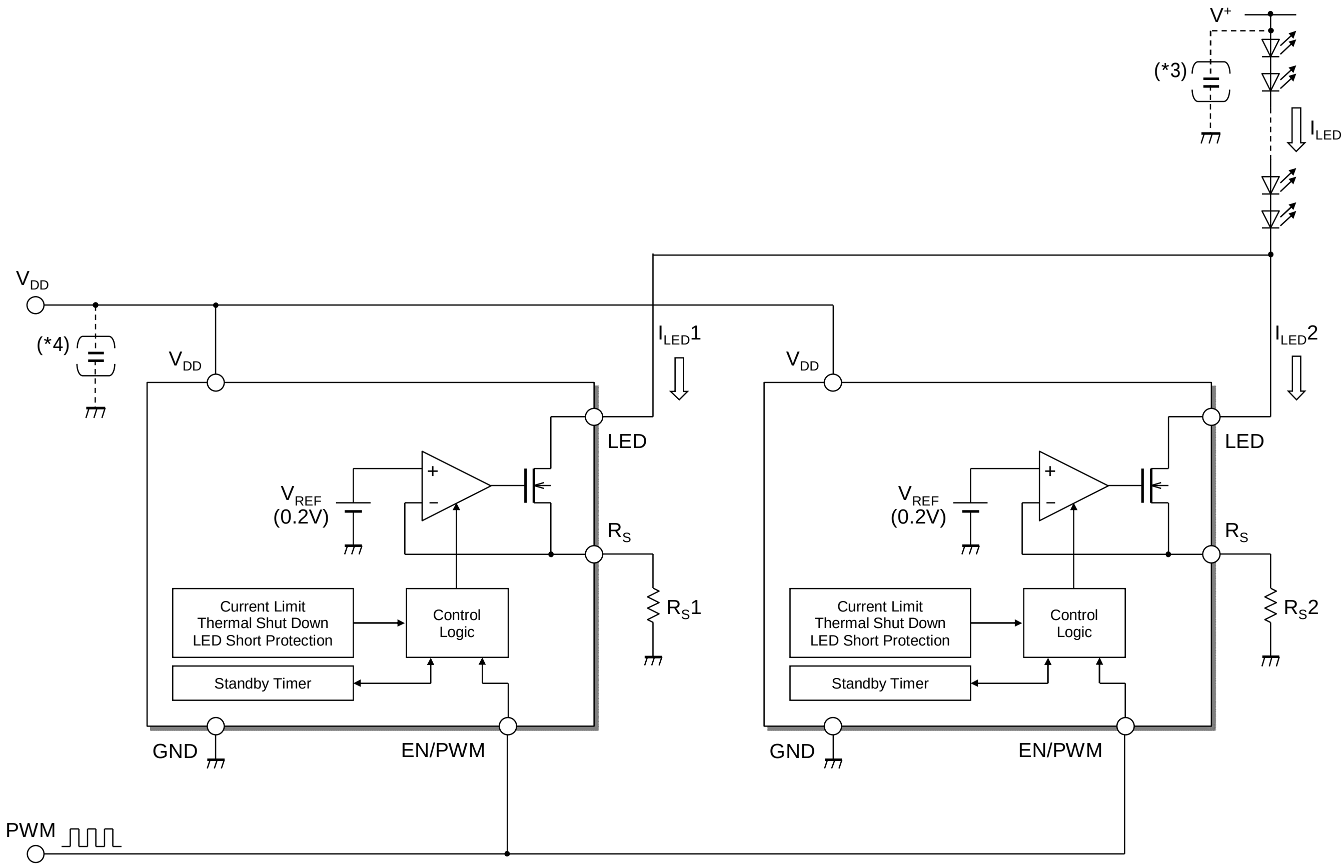
(For 4Layers: Applying 74.2 × 74.2mm inner Cu area and a thermal via hole to a board based on JEDEC standard JESD51-5)

■ Parallel Drive of NJW4617

For applications that require more than 500mA, it can correspond by parallel connecting two or more NJW4617.
The LED current becomes sum of LED current of each NJW4617.
Each device accepts different set current.

I_{LED} [A] = $I_{LED}1 + I_{LED}2 = 0.2 / R_S1$ [Ω] + $0.2 / R_S2$ [Ω]

(e.g. I_{LED} = 750 [mA] setting: R_S1 = 0.4 [Ω], R_S2 = 0.8 [Ω])



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