NJRC's OPAmp Line-up, featuring the NJM411 series

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NJM411A/11/12

NJRC Low Offset, Low Drift JFET Input OPAmp series

This newsletter talks about NJRC'S **Low Offset, Low Drift JFET Input OPAmp** , the NJM411A/11/12.

■ FEATURES

- Low Operating Voltage: V_{opr}= ±5.0V to ±16V
- Low Offset Voltage: V_{IO}=0.5mV max. **(NJM411A)**
  V_{IO}=1mV max. **(NJM411)**
  V_{IO}=2mV max. **(NJM412)**
- Low Offset Voltage: $\Delta V_{IO}/\Delta T$=10mVdeg **(NJM411A)**
  $\Delta V_{IO}/\Delta T$=20mVdeg **(NJM411/12)**
- Low Input Bias Current: I_B=200pA max. @Ta=25° C
- Voltage Gain: Av=106dB typ.
- Slew Rate: SR=15.0V/μs typ.
- Bipolar Technology
- Package: DIP8, DMP8, EMP8(Plan)

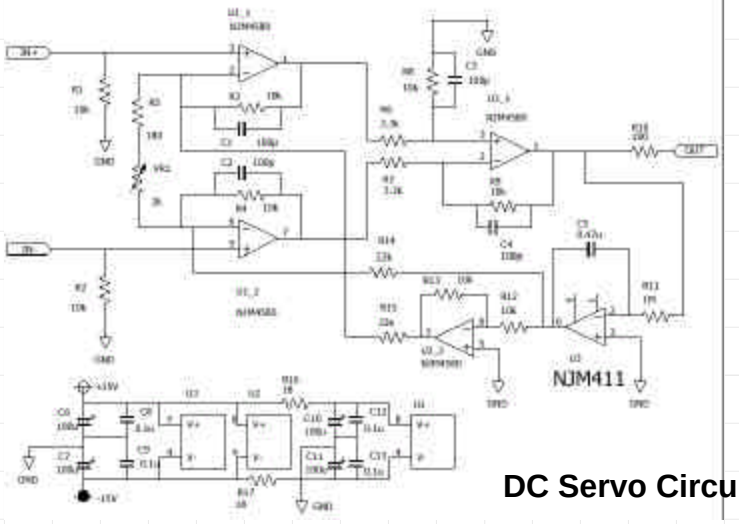
■ APPLICATIONS

Industrial Equipments,
Measurement Equipments,
and so on.

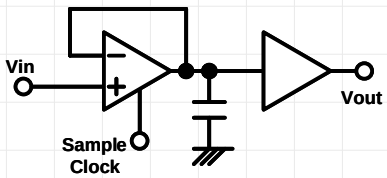
Competitor
NS: LF411/12
ADI: AD711/12

Direct Replacement of NJM062/072B/082B and
Upper Compatible of NJM062/072B/082B

■ EXAMPLE APPLICATION CIRCUITS



DC Servo Circuit



Sample Hold

Other: High-Speed Integrator
High-Speed Buffer

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NJR CORPORATION offers Bipolar ICs, CMOS ICs, BiCMOS ICs and GaAs MMICs as well as Saw filters, covering North and South America to provide technical assistance and quick delivery for achieving customer satisfactions. For further information, please contact:

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